ABSTRACT OF THE DISCLOSURE

In a liquid crystal display element provided with a liquid crystal layer having a memory function, $4.0 \leqq a \leqq d \cdot V/10$ is satisfied where a represents a space (an interline width) between transparent electrodes 31, 31 adjacent to each other on the same surface of a substrate 3, d (μm) represents the thickness of the liquid crystal layer interposed between the transparent electrodes 21, 31 (a pixel portion D) opposing between upper and lower substrates 2, 3, V_{MAX} represents the maximum effective voltage required to change a display, and a (μm) represents the maximum space of the transparent electrodes whereby a uniform alignment state in a pixel portion and an interline portion can be obtained.